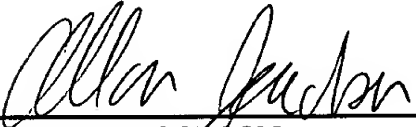


REMARKS

The examiner has allowed claims 4, 6, 10 and 11 and rejected claims 1-3, 5, 7-9 and 12-13 under 35 U.S.C. §103(a) as being unpatentable over the publication "A Parallel Pipelined DSP Processor Core" by Aikens et al.

In response, applicant hereby cancels claims 1-3, 5, 7-9 and 12-13. It is believed that this application is in condition for allowance, and a notice of allowance of claims 4, 6, 10 and 11 is solicited.

Respectfully submitted,  
by

  
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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on

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ALLAN J. JACOBSON

Name of registered representative

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